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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fred Fishburn

Title: CONTACT STRUCTURE

Docket No.: 303.703US1

Serial No.: 09/653,640

Filed: August 31, 2000

Due Date: April 14, 2002

Examiner: Alexander Williams

Group Art Unit: 2826

Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- A return postcard.
 A Response to Restriction Requirement and Preliminary Amendment (3 Pages).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: David R. Cochran
Atty: David R. Cochran
Reg. No. 46,632

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 15 day of April, 2002.

Name

Tina Rugh

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

(GENERAL)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Fred Fishburn
Serial No.: 09/653,640
Filed: August 31, 2000
Title: CONTACT STRUCTURE

Examiner: Alexander Williams
Group Art Unit: 282625 232
Docket: 303702US1
CEIVED
MAIL ROOM

RESPONSE TO RESTRICTION REQUIREMENT
AND PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

*#8Electronics
J.B.
5/1/02*

In response to the Restriction Requirement mailed March 14, 2002, Applicant elects, without traverse, Group I (claims 19-36). Applicant respectfully cancels remaining claims 1-18 and 37-60 (Groups II and III) without prejudice, and reserves the right to reintroduce them in a divisional application at a later date.

IN THE CLAIMS

Please add the following new claims:

- SUB C3* 94. (New) An integrated circuit comprising:
a first device;
a second device;
a contact coupling the first device to the second device; and
an barrier structure of silicon nitride encircling the contact.
- Multilayer* 95. (New) The integrated circuit of claim 94, wherein the first device is a capacitor.
96. (New) The integrated circuit of claim 94, wherein the second device is a transistor.
- SUB C4* 97. (New) An integrated circuit comprising:
a first device;
a second device;
one or more layers coupling the first device to the second device, at least one of the one